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| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
| 10/698,711      | 10/31/2003  | Wen Chuan Chen       | CC-CFP-1307         | 1557             |

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EXAMINER

ORTIZ, EDGARDO

ART UNIT PAPER NUMBER

2815

DATE MAILED: 01/21/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

|                              |                                      |                                    |  |
|------------------------------|--------------------------------------|------------------------------------|--|
| <b>Office Action Summary</b> | <b>Application No.</b><br>10/698,711 | <b>Applicant(s)</b><br>CHEN ET AL. |  |
|                              | <b>Examiner</b><br>Edgardo Ortiz     | <b>Art Unit</b><br>2815            |  |

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --**

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 05 November 2004.
- 2a) ☒ This action is **FINAL**.                      2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-4 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-4 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All    b) ☐ Some \*    c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- |  |   |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892)   | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                   | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)             |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____  |

**DETAILED ACTION**

***Claim Rejections - 35 USC § 102***

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-4 are rejected under 35 U.S.C. 102(b) as being anticipated by Crane, Jr. et al. (U.S.

Patent No. 6,307,258). With regard to Claim 1, Crane discloses:

a base plate (200) on which IC chips (100) are connected;

a case (400) mounted to the base plate and at least one window (410) defined through the case so that at least one of the IC chips is accommodated in the at least one window (column 4, lines 27-30), a space enclosed by the at least one window being larger than the at least one of the IC chips (column 4, lines 28-32) and a top surface of the at least one of the IC chips accommodated in the at least one window being in flush with a top surface of the case (column 4, lines 34-40), and

a seal plate (600) attached on the case and sealing the at least one window (column 9, lines 6-10). See figures 1, 12 and 16.

With regard to Claim 2, Crane discloses IC chips having a connection legs electrically connected to the base plate (200), (column 4, lines 11-15).

Art Unit: 2815

With regard to Claim 3, Crane discloses a top surface of the at least one of the IC chips accommodated in the at least one window (410) in flush with a top surface of the case (400), (column 4, lines 34-38).

With regard to Claim 4, Crane discloses:

a base plate (200) on which IC chips (100) are connected;

a case (400) mounted to the base plate and at least one window (410) defined through the case so that at least one of the IC chips is accommodated in the at least one window (column 4, lines 27-30), a space enclosed by the at least one window being slightly larger than the at least one of the IC chips (column 4, lines 28-32) and a top surface of the at least one of the IC chips accommodated in the at least one window being in flush with a top surface of the case (column 4, lines 34-40), a periphery of at least one of the IC chips being fitted and in contact with an inner periphery of the at least one window (column 4, lines 34-39), and

a seal plate (600) attached on the case and sealing the at least one window (column 9, lines 6-10). See figures 1, 12 and 16.

### ***Response to Arguments***

2. Applicant's arguments, filed November 5, 2004, have been fully considered however they are not deemed persuasive for the reasons stated in the body of the office action. Applicant first argues that *"a limitation is added in the amended claim 1 and defined that the space that is enclosed by the at least one window is larger than the at least one chips so that the whole at least one chip can be accommodated in the space enclosed by the at least one window"*. The examiner

Art Unit: 2815

first notes that Applicant does not claim a “*whole at least one chip*” accommodated in the space enclosed by the at least one window. The claim merely discloses “*at least one of the IC chips is accommodated in the at least one window*”, and as such, Crane clearly discloses the claimed case (400) mounted to a base plate and at least one window (410) defined through the case so that at least one of the IC chips is accommodated in the at least one window (column 4, lines 27-30).

Applicant also argues that new claim 4 “*further includes that a periphery of the at least one of the IC chips is fitted and in contact with an inner periphery of the at least one window. This is totally different from the disclosure of Crane Jr. et al.*” The examiner also disagrees with Applicant’s assertion of the Crane reference and notes that the reference clearly discloses the claimed periphery of at least one of the IC chips being fitted and in contact with an inner periphery of the at least one window (column 4, lines 34-39).

Therefore, the claimed invention does not patentably or structurally distinguish from that disclosed by the prior art, as shown by Crane et al., and the rejection should be maintained.

### ***Conclusion***

3. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after

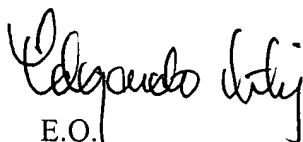
Art Unit: 2815

the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Edgardo Ortiz whose telephone number is 571-272-1735. The examiner can normally be reached on Monday-Friday (1st Friday Off).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas can be reached on 571-272-1664. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



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